

CLAIMS

What is claimed is:

1. A semiconductor package for use in a peripheral device card connectable to a
5 bus of a host computer the semiconductor package including:
non-volatile memory for storing a card information structure; and
bus decode logic.
2. A semiconductor package accordingly to claim 1, wherein the semiconductor
10 package provides functions conformant with a Personal Computer Memory Card
International Association (PCMCIA) standard.
3. A semiconductor package according to claim 1, wherein the semiconductor
package is a multi-chip module with the non-volatile memory and the bus decode
15 logic being realized in respective first and second chips of the multi-chip module.
4. A semiconductor package according to claim 1, wherein the non-volatile
memory and bus decode logic are realized on a single semiconductor substrate.
- 20 5. A semiconductor package according to claim 1, wherein the semiconductor
package further includes circuitry associated with an operating function.
6. A semiconductor package according to claim 5, wherein the circuitry provides
a universal asynchronous receiver-transmitter (UART) operating function.
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7. A peripheral device card for connecting to a bus of a host computer, the card
including:
non-volatile memory for storing a card information structure; and
bus decode logic; wherein the non-volatile memory and the bus decode logic
30 are realized in a single semiconductor package.

8. A peripheral device card accordingly to claim 7, wherein the card conforms to a Personal Computer Memory Card International Association (PCMCIA) standard.

9. A peripheral device card according to claim 7, wherein the non-volatile
5 memory and the bus decode logic are realized in respective first and second chips in a multi-chip module.

10. A peripheral device card according to claim 7, wherein the first and second areas of non-volatile memory are realized on a single semiconductor substrate.

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11. A peripheral device card according to claim 7, wherein the single semiconductor package further includes circuitry associated with an operating function of the card.

15 12. A peripheral device card according to claim 12, wherein the circuitry provides a universal asynchronous receiver-transmitter (UART) operating function of the card.

13. A peripheral device card according to claim 12, wherein the circuitry provides all operating functions of the card.